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IN THE SPECIFICATION:

Please delete the paragraph beginning on page 7, line 22, and replace with the following new paragraph. Changes from the original paragraph are highlighted.

Turning now to FIG. 3, the LBIST state machine 210 has five primary states: a reset state 310, an initialization state 320, a scan state 330, a step state 340, and a done state 350. The LBIST state machine 210 is reset, i.e., transitions to the reset state 310, whenever an external reset signal is asserted regardless of which state in which it might be. On transition to the reset state 310, the MISR 220 and the pattern generator 230 are initialized. The LBIST state machine 210 remains in the reset state 310 until the LBIST RUN signal is received, whereupon it transitions to the initiate state 320. In the initiate state 320, the LBIST initiates the various signals to be used in the LBIST. For instance, the COUNTER(S), COMPLETE, and ERROR signals, whose functions shall be discussed more fully below, are initialized. The LBIST state machine 210 then automatically transitions to the scan state 330 and begins to repeatedly cycle through the scan state 330 and the step state 340 (which, in one embodiment, includes stepping to a new scan chain). Note that, in the early cycles, the scan state ~~340~~ 330 flushes the scan chains (not shown) and the MISR 220 is not loaded, in the illustrated invention, until after the scan chains flush.